

FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 005231/28-0053BS		SERIAL NO. 09/626,325	
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				APPLICANT John S. Yates, Jr., et al.		GROUP ART UNIT 2183	
				FILING DATE July 26, 2000			
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
RUE	3,544,969	12/01/1970	Rakoczi	340	172.5	Nov. 27, 1967	
RUE	4,084,235	04/11/1978	Hirtle	364	200	Apr. 14, 1975	
RUE	4,455,602	06/19/1984	Baxter	364	200	May 22, 1981	
RUE	4,514,803	04/30/1985	Agnew	364	200	Apr. 26 1982	
RUE	4,575,797	03/11/1986	Cruner	364	200	May 22, 1981	
RUE	4,779,187	10/18/1988	Letwin	—	—	Apr. 10, 1985	
RUE	5,155,835	10/13/1992	Belsan	—	—	Nov. 19, 1990	
RUE	5,241,664	08/31/1993	Ohba	—	—	Jan. 7, 1991	
RUE	5,301,287	04/05/1994	Herrell	—	—	Feb. 16, 1993	
RUE	5,361,340	11/01/1994	Kelly	—	—	Mar. 9, 1993	
RUE	5,432,795	07/11/1995	Robinson	371	19	May 15, 1991	
RUE	5,454,117	09/26/1995	Puziol	395	800	Aug. 25, 1993	
RUE	5,515,518	06/07/1996	Stiles	395	375	Jul. 5, 1994	
RUE	5,542,059	07/30/1996	Blomgren	395	375	Dec. 21, 1994	
RUE	5,542,109	07/30/1996	Blomgren	395	800	Aug. 31, 1994	
RUE	5,574,927	11/12/1996	Scantlin	395	800	Nov. 12, 1996	
RUE	5,608,886	03/04/1997	Blomgren	395	586	Aug. 31, 1994	
RUE	5,628,016	05/06/1997	Kukol	395	705	June 15, 1994	
RUE	5,636,366	06/03/1997	Robinson	395	490	Oct. 30, 1995	
RUE	5,659,679	08/19/1997	Alpert	395	183.1	May 30, 1995	
RUE	5,664,159	09/02/1997	Richter	395	500	May 8, 1995	
RUE	5,715,421	02/03/1998	Akiyama	—	—	Oct. 16, 1992	
RUE	5,721,927	02/24/1998	Baraz	395	705	Aug. 7, 1996	
RUE	5,754,878	05/19/1998	Asghar	395	800	Mar. 18, 1996	
RUE	5,774,287	06/30/1998	Leonhardt	—	—	Dec. 9, 1994	
RUE	5,774,686	06/30/1998	Hammond	395	385	Jun. 7, 1995	
RUE	5,781,792	07/14/1998	Asghar	395	800.35	Mar. 18, 1996	
RUE	5,784,640	07/21/1998	Asghar	395	800.35	Mar. 18, 1996	
RUE	5,790,824	08/04/1998	Asghar	395	385	Mar. 18, 1996	
RUE	5,794,068	08/11/1998	Asghar	395	800.35	Mar. 18, 1996	
RUE	5,796,939	08/18/1998	Berc, et al.	395	184.01	Mar. 10, 1997	
RUE	5,802,272	09/01/1998	Sites, et al.	395	183.21	Dec. 19, 1994	
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RUE	5,822,578	10/13/1998	Frank	395	591	June 5, 1995	
RUE	5,832,205	11/03/1998	Kelly	395	185.06	Aug. 20, 1996	
RUE	5,838,962	11/17/1998	Larson	395	586	Apr. 9, 1997	
RUE	5,838,978	11/17/1998	Buzbee	395	705	Oct. 9, 1996	
RUE	5,847,955	12/08/1998	Mitchell	—	—	Jun. 23, 1997	
RUE	5,884,057	03/16/1999	Blomgren	395	375	May 8, 1995	
RUE	5,901,308	05/04/1999	Cohn	—	—	Mar. 18, 1996	
RUE	5,905,855	05/18/1999	Klaiber	395	183.07	Feb. 28, 1997	
RUE	5,935,220	08/10/1999	Lumpkin	—	—	Aug. 9, 1996	
RUE	5,949,415	09/07/1999	Lin	345	335	Jun. 16, 1997	
RUE	5,958,047	09/28/1999	Panwar	712	237	Jun. 25, 1997	
RUE	5,958,061	09/28/1999	Kelly	714	1	Jul. 24, 1996	
RUE	5,999,736	12/07/1999	Gupta	—	—	May 9, 1997	
RUE	6,003,038	12/14/1999	Chen	707	103	March 31, 1997	
RUE	6,005,551	12/21/1999	Osborne	—	—	Apr. 25, 1997	
RUE	6,011,908	01/04/2000	Wing	395	182.17	Dec. 23, 1996	
RUE	6,014,735	01/11/2000	Chennupaty	712	210	Mar. 31, 1998	
RUE	6,021,489	02/01/2000	Poplingher	712	239	Jun. 30, 1997	
RUE	6,026,478	02/15/2000	Dowling	—	—	Dec. 23, 1997	
RUE	6,029,004	02/22/2000	Bortnikov	—	—	Mar. 17, 1997	
RUE	6,032,247	02/29/2000	Asghar	712	35	Nov. 14, 1997	
RUE	6,035,393	03/07/2000	Glew	712	237	May 7, 1998	
RUE	6,044,221	03/28/2000	Gupta	—	—	May 9, 1997	
RUE	6,070,009	05/30/2000	Dean	—	—	Nov. 26, 1997	
RUE	6,081,665	06/27/2000	Nilsen et al.	395	705	Dec. 19, 1997	
RUE	Re. 36,766	07/04/2000	Krauskopf	711	203	Aug. 21, 1992	
RUE	6,154,857	11/28/2000	Mann	—	—	Dec. 17, 1997	
RUE	6,157,970	12/05/2000	Gafken	—	—	Sep. 24, 1997	
RUE	6,175,957	01/16/2001	Ju	—	—	Dec. 9, 1997	
RUE	6,226,789	05/01/2001	Tye	—	—	Jan. 29, 1996	
RUE	6,237,073	05/22/2001	Dean	—	—	Nov. 26, 1997	
RUE	6,240,526	05/29/2001	Petivan	—	—	Oct. 27, 1999	
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U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
RUE	6,253,215	06/26/2001	Agesen	—	—	Aug. 17, 1998	
RUE	6,351,784	02/26/2002	Neal	—	—	Dec. 28, 1998	
RUE	6,374,369	04/16/2002	O'Donnell	—	—	May 21, 1999	
RUE	6,397,379	05/28/2002	Yates	—	—	Oct. 28, 1999	
RUE	6,405,327	06/11/2002	Sipple	—	—	Aug. 19, 1998	
RUE	6,463,582	10/08/2002	Lethin	—	—	Oct. 21, 1998	
RUE	6,470,492	10/22/2002	Bala	—	—	May 14, 1999	
RUE	6,477,683	11/05/2002	Killian	—	—	Feb. 5, 1999	
RUE	6,480,862	11/12/2002	Gall	—	—	Apr. 23, 1999	
RUE	6,496,922	12/17/2002	Borrill	—	—	Oct. 31, 1994	
FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
RUE	WO 98/59292 Emelik	12/30/1998	PCT/US ^{WIPO}	G06F	9/45		
RUE	WO 99/08188 Kelly	02/18/1999	PCT/US ^{WIPO}	G06F	11/00		
RUE	WO 99/08191 Kelly	02/18/1999	PCT/US ^{WIPO}	G06F	12/00		
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)							
RUE	Burroughs, Preliminary Edition, B 1700 Systems Reference Manual, Burroughs Corporation (1972)						
RUE	Burroughs, B 1700 Systems Micro Implementation Language (MIL) Reference Manual, Burroughs Corporation (1973)						
RUE	Anton Chernoff, et al., FX132, A Profile-Directed Binary Translator, IEEE Micro, Vol. 18 No. 2 pp. 56-64 (March/April, 1998)						
RUE	T.M. Conte, et al., "Accurate and practical profile-driven compilation using the profile buffer," Proceedings of the 29 th Annual International Symposium on Microarchitecture, Paris, France, pp. 36-45 (Dec. 1996)						
RUE	T.M. Conte, et al., "Hardware based profiling: An effective technique for profile-driven optimization," International Journal of Parallel Programming, vol. 24, no. 2 (Feb. 1996)						
RUE	T.M. Conte, et al., "Using branch handling hardware to support profile-driven optimization," Proceedings of the 27 th Annual International Symposium on Microarchitecture San Jose, CA (Dec. 1994)						
RUE	J.S. Cox et al., "Commercializing profile-driven optimization," Proceedings of the 28 th Hawaii International Conference on System Sciences, vol. 1 Maui, HI, pp. 221-228 (Jan. 1995)						
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)					
RLE		Ebcioğlu and Altman, IBM Research Report, DAISY: Dynamic Compilation for 100% Architectural Compatibility, IBM Research Division (1996)			
RLE		Ronald M. Guffin, Microdiagnostics for the Standard Computer MLP-900 Processor, IEEE Transactions on Computers, Vol. C-20 No. 7, pp. 803-808 (July 1971)			
RLE		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64 Technology and Market Forecast, IA-64 Software Model - Chapter 3 excerpt Microprocessor Report, MicroDesign Resources (July 20, 1998)			
RLE		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64: Technology and Market Forecast, Executive Summary, Microprocessor Report, MicroDesign Resources (July 20, 1998)			
RLE		Linley Gwennap, First Merced Patent Surfaces, Intel Document Reveals Processors With Dual Instruction Sets, Microprocessor Report, MicroDesign Resources (July 20, 1998)			
RLE		R.E. Hank, et al., Proceeding of the 28 th Annual International Symposium on Microarchitecture, pp. 158-168 (Dec. 1995)			
RLE		Raymond J. Hookway, et al., Combining Emulation and Binary Translation (August 28, 1997) http://www.digital.com/info/DIJP01HM.HTM			
RLE		Intel Corporation, Intel Architecture Software Developer's Manual, <i>passim</i> (1997)			
RLE		Kavi et al., A Performability Model for Soft Real-Time Systems, IEEE Jan 1994			
RLE		Harold W. Lawson, Jr., et al., Functional Characteristics of a Multilingual Processor, IEEE Transactions on Computers, Vol. C-20, No. 7, pp. 732-742 (July, 1971)			
RLE		K.N.P. Menezes, "Hardware-based profiling for program optimization," Ph.D. thesis, Department of Electrical and Computer Engineering, North Carolina State University, Raleigh, North Carolina (1997)			
RLE		Nanodata Corporation, QM-1 Hardware level User's Manual (1975)			
RLE		Nanodata Corporation, QM-2 Processor (1976)			
RLE		Robert F. Rosin, Contemporary Concepts of Microprogramming and Emulation, Computing Surveys, Vol. 1 No. 4, pp. 197-212 (December 1969)			
RLE		Robert F. Rosin, An Environment for Research in Microprogramming and Emulation, Communications of the ACM, Vol. 15 No. 8, pp. 748-760 (August 1972)			
RLE		Standard Computer Corp., IC-9000 Central Processing Unit - Principles of Operation (undated - possibly 1969)			
RLE		W.T. Wlner, Design of the Burroughs B1700, Fall Joint Computer Conference, pp. 489-497 (1972)			
RLE		W.T. Wlner, Burroughs B1700 Memory Utilization, Fall Joint Computer Conference, pp. 579-586 (1972)			
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FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 005231.0028		SERIAL NO. 09/626,325	
<div style="position: relative; height: 100px;"> <div style="position: absolute; top: 0; left: 0; width: 100%; text-align: center;"> INFORMATION DISCLOSURE CITATION (Use several sheets if necessary) </div> <div style="position: absolute; bottom: 0; left: 0; width: 100%; text-align: center;"> U.S. PATENT DOCUMENTS </div> </div>				APPLICANT John S. Yates, Jr., et al.			
				FILING DATE January 28, 2000		GROUP ART UNIT 2183	

EXAMINER	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
RLE	4,779,187	10/18/1988	Letwin	—	—	—
RLE	4,812,975	03/14/1989	Adachi et al.	—	—	—
RLE	5,115,500	05/19/1992	Larsen	—	—	—
RLE	5,335,331	08/02/1994	Murao et al.	—	—	—
RLE	5,568,646	10/22/1996	Jaggar	—	—	—
RLE	5,619,666	04/08/1997	Coon et al.	—	—	—
RLE	5,678,032	10/14/1997	Woods et al.	—	—	—
RLE	5,751,982	05/12/1998	Morley	—	—	—
RLE	5,781,750	07/14/1998	Blomgren et al.	—	—	—
RLE	6,006,029	12/21/1999	Bianchi et al.	—	—	—
RLE	6,021,265	02/01/2000	Nevill	—	—	—
RLE	6,163,764	12/19/2000	Dulong et al.	—	—	—
RLE	6,219,774	04/17/2001	Hammond et al.	—	—	—

FOREIGN PATENT DOCUMENTS							
EXAMINER	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)			

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